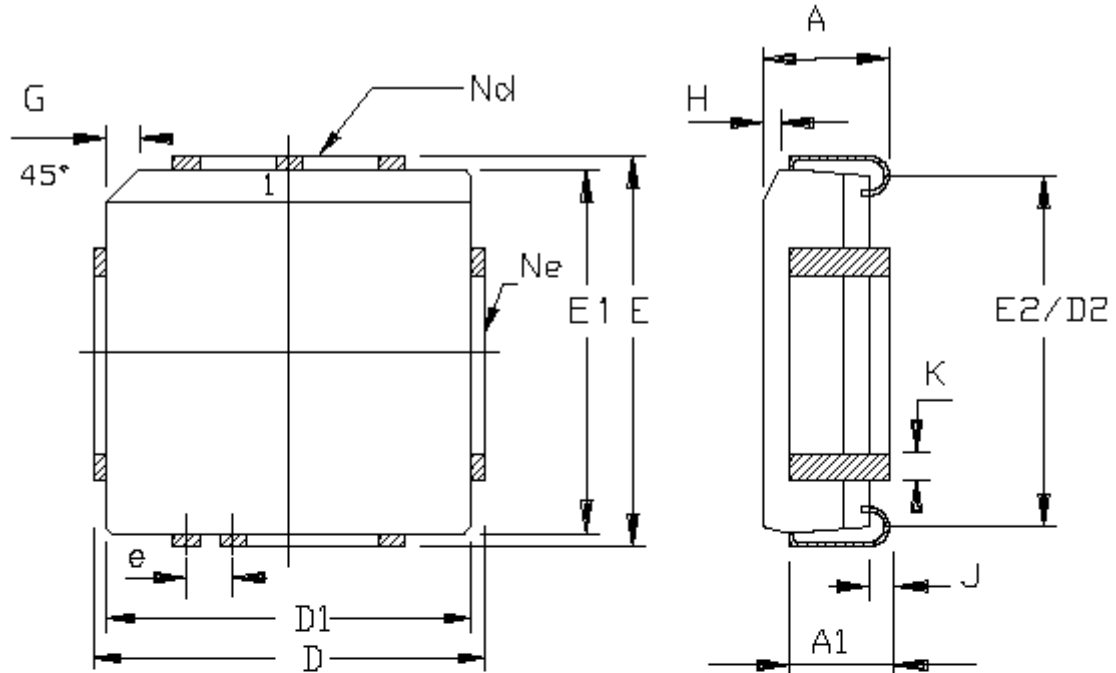


AT89C51xD2 wiring scheme for 40/48 pin DIL to PLCC68.

GALEP DIL40	Adapter PLCC68	Pin name
1	19	P1.0
2	21	P1.1
3	22	P1.2
4	23	P1.3
5	25	P1.4
6	27	P1.5
7	28	P1.6
8	29	P1.7
9	30	RST
10	34	P3.0
11	39	P3.1
12	40	P3.2
13	41	P3.3
14	42	P3.4
15	43	P3.5
16	45	P3.6
17	47	P3.7
18	48	XTAL2
19	49	XTAL1
20	51	VSS
21	54	P2.0
22	55	P2.1
23	56	P2.2
24	58	P2.3
25	59	P2.4
26	61	P2.5
27	64	P2.6
28	65	P2.7
29	67	PSEN
30	68	ALE/PROG
31	2	EA
32	3	P0.7
33	5	P0.6
34	6	P0.5
35	9	P0.4
36	11	P0.3
37	12	P0.2
38	14	P0.1
39	15	P0.0
40	17	VCC

PLCC68



	MM		INCH	
	A	4.20	5.08	.165
A1	2.29	3.30	.090	.130
D	25.02	25.27	.985	.995
D1	24.13	24.33	.950	.958
D2	22.61	23.62	.890	.930
E	25.02	25.27	.985	.995
E1	24.13	24.33	.950	.958
E2	22.61	23.62	.890	.930
e	1.27	BSC	.050	BSC
G	1.07	1.22	.042	.048
H	1.07	1.42	.042	.056
J	0.51	-	.020	-
K	0.33	0.53	.013	.021
Nød	17		17	
Ne	17		17	
PKG STD		00		